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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
0 375,628	09/29/2000	Chuxin Chen	28787.3	9579	
7590 08/31/2004			EXAMINER		
David L McCombs			BAUTISTA, XIOMARA L		
Haynes and Boo Suite 3100	one, L.L.P.		ÀRT UNIT	PAPER NUMBER	
901 Main Street	=		2179		
Dallas, TX 75	5202-9918		DATE MAILED: 08/31/2004	11	

Please find below and/or attached an Office communication concerning this application or proceeding.



	Application No.	Applicant(s)	<u>\(\) \(\) \(\) \(\)</u>
Advisory Action	09/675,628	CHEN ET AL.	
Auvisory Action	Examiner	Art Unit	
	X L Bautista	2179	
The MAILING DATE of this communication	n appears on the cover sheet w	vith the correspondence addr	ess
THE REPLY FILED FAILS TO PLACE THI Therefore, further action by the applicant is require final rejection under 37 CFR 1.113 may only be eit condition for allowance; (2) a timely filed Notice of Examination (RCE) in compliance with 37 CFR 1.1	ther: (1) a timely filed amendn Appeal (with appeal fee); or (is application. A proper rep nent which places the applic	ation in
PERIOD FO	OR REPLY (check either a) or	b)]	
a) The period for reply expiresmonths from the			
b) The period for reply expires on: (1) the mailing date of event, however, will the statutory period for reply expire ONLY CHECK THIS BOX WHEN THE FIRST REPL 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). have been filed is the date for purposes of determining the period 37 CFR 1.17(a) is calculated from: (1) the expiration date of the sl (b) above, if checked. Any reply received by the Office later than the slight of the	e later than SIX MONTHS from the main Y WAS FILED WITHIN TWO MONTH The date on which the petition under 3 of extension and the corresponding ame thortened statutory period for reply origin	ling date of the final rejection. IS OF THE FINAL REJECTION. So OF CFR 1.136(a) and the appropriate out of the fee. The appropriate extended to the fee. The appropriate extended to the final Office action; or (extension fee ension fee under 2) as set forth in
earned patent term adjustment. See 37 CFR 1.704(b). 1. A Notice of Appeal was filed on App	pellant's Brief must be filed wit	hin the period set forth in	
37 CFR 1.192(a), or any extension thereof (smissai oi trie appeai.	
2. The proposed amendment(s) will not be ent		search (see NOTE below):	
(a) they raise new issues that would require		search (see NOTE below),	
(b) ☐ they raise the issue of new matter (see(c) ☐ they are not deemed to place the application		I by materially reducing or s	implifying the
(c) ☐ they are not deemed to place the applic issues for appeal; and/or	Jation in better form for appea	i by materially reducing or s	ampinying the
(d) they present additional claims without	canceling a corresponding nu	mber of finally rejected clain	ns.
NOTE:			
3.☐ Applicant's reply has overcome the following	g rejection(s):		
4. Newly proposed or amended claim(s) canceling the non-allowable claim(s).	would be allowable if submitt	ed in a separate, timely filed	l amendment
5.⊠ The a)□ affidavit, b)□ exhibit, or c)⊠ requapplication in condition for allowance becau		een considered but does NO	T place the
6. The affidavit or exhibit will NOT be consider raised by the Examiner in the final rejection		SOLELY to issues which wer	re newly
7. For purposes of Appeal, the proposed amer explanation of how the new or amended cla			and an
The status of the claim(s) is (or will be) as for	ollows:		

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10. Other: ____

Claim(s) allowed: _____.
Claim(s) objected to: _____.
Claim(s) rejected: _____.

Claim(s) withdrawn from consideration: _____.

8. The drawing correction filed on ____ is a) approved or b) disapproved by the Examiner.

9. Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s).

X L Bautista Patent Examiner Art Unit: 2179



Application No.

Continuation of 5. does NOT place the application in condition for allowance because: Applicant argues that Grau teaches a physical network not a logical network. According to the definition of logical network in Exhibit A, a logical network is a way to describe the topology, or layout of a computer network; it describes the way information moves through the network; it never says that a logical network is actually a NETWORK, just a way to describe the topology. Grau discloses a network mapping tool for creating an atlas (topology) and displaying topology data that facilitates comprehension of logical relationships between components of a system. Grau teaches a physical and logical network.